Inventor:

Zachary B. Katz

Title:

Methods of Forming Planarized Surfaces Over Semiconductor

Substrates

Assignee:

Micron Technology, Inc.

## INFORMATION DISCLOSURE STATEMENT

References - See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR § 1.56. Copies of the cited art are included with the exception of U.S. patents and published U.S. applications (1276 OG 55). No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

Dated:

JANUARY 26, 2004

By:

David G Latwesen, Ph.D.

Reg. No. 38,533

Form PTO-1449	<del></del>	<del>-</del> -		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE				ATTY, DOCKET NO. MI22-2445				SERIAL NO. Filed Herewith		
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)							APPLICANT Micron Technology, Inc.							
								FILING DATE Filed Herewith			GROUP Unknown			
			<del></del>		U.S. PA	TENT DOCUMENTS	•						<u> </u>	
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	AA	6.649	2.503	1/18/03	Kim et al.									
	AB	6.635.586		10/21/03	Goo et al.									
	AC	6.596.607		7/22/03	Ahn									
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	AH	6,184,143		2/6/01	Ohashi et al.									
FOREIGN PATENT DOCUMENTS														
		Document Number		Date	Country		Class	Subclass		Translation				
	AI			-							Yes	No		
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)														
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*Examiner Initial		Document Number		Date	Name	Class	Subcl	obclass Filing If Appr		Date opriate				
	AA	6,258	3.649	7/10/01	Nakamura et al.									
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	AF	5,169	),491	12/8/92	Doan									
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